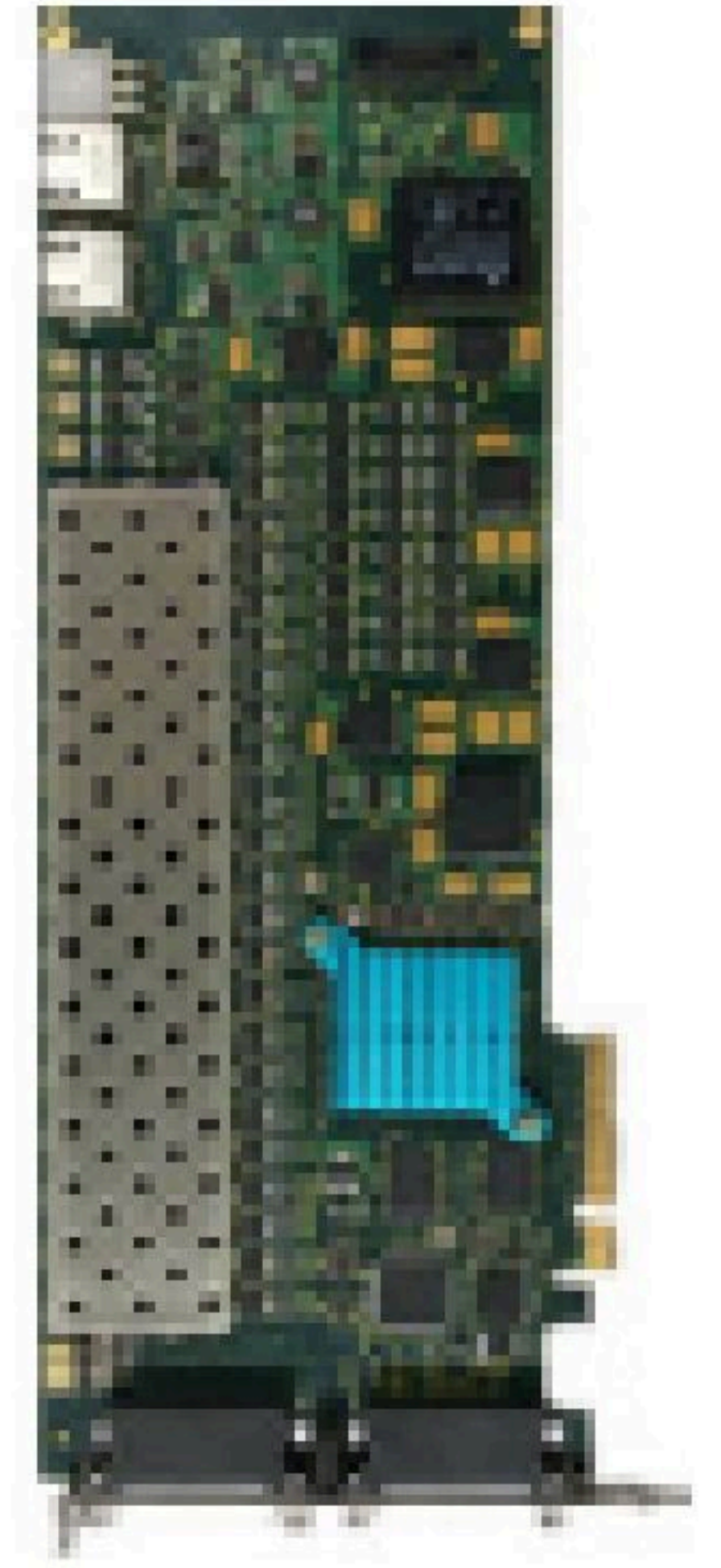


LVDT/RVDT Simulation Package

Concurrent Real-Time's LVDT/RVDT Simulation package is based on a customized version of the company's Altera Arria V Family FPGA card with 362K logic elements, designed especially for Hardware-in-the-Loop (HIL) testing. The solution provides eight 3/4-wire LVDT/RVDT channels with transformer coupling. The card is supported by Concurrent's Simulation Workbench™ (SimWB), a comprehensive framework for developing and executing realtime HIL simulations.



Features:

- ◆ Eight 3/4-wire LVDT/RVDT simulation channels
- ◆ Ratiometric Support
- ◆ Programmable Transformation Ratio
- ◆ Isolated Power on all I/O
- ◆ CQ9500TR-08 8-channel 3/4-wire LVDT/RVDT/Resolver transformer coupling signal conditioning module with DIN mount and 2 meter interface cable
- ◆ Up to ±20V differential input/output
- ◆ 10k Ohm impedance
- ◆ Frequency Response: + 2.0 dB, at 300 Hz to 100K Hz (1 KHz Ref.)
- ◆ Includes eight FPGA Workbench LVDT/RVDT TX licenses (ICS-FPGA-2003)

About Concurrent Real-Time: Concurrent Real-Time is the industry's foremost provider of high-performance real-time computer systems, solutions and software for commercial and government markets.



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Specifications:

Form Factor

- PCIe x4 Rev 1.0a
- Full Height, Full Length PCIe (12.3" x 3.8")

Power Requirements

Approximately 25 watts (3.3VDC @ 4 Amps, 12VDC @ 1.2 Amp)

Environmental

- Operating Temp: 10° C to 40° C
- Storage Temp: -40° C to 65° C
- Relative Humidity: 10 to 80% non-condensing
- ROHS Compliant

Ordering Information:

- ◇ **CP-LVDT-RVDT-SIM**
8-channel LVDT/RVDT Simulation Package
- ◇ **WC-CP-FIO**
RedHawk™ Driver for FPGA-based I/O cards
- ◇ **ICS-SWB-1276**
License for Programmable FPGA card use under SIMulation Workbench

Note: The CP-LVDT-RVDT-SIM model number supports the above functionality only. If additional features are required, a programmable FPGA PCIe card (CP-FPGA-2) is required.